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PATENT
8040-1022

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Hiroataka NAKANO et al.

Serial No. 09/855,723

Filed May 16, 2001

Conf. 9420

Group 2825

Examiner M. Dimyan

SYSTEM OF MANUFACTURING
SEMICONDUCTOR INTEGRATED CIRCUIT

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 4, 2004

Sir:

The undersigned hereby petitions for an extension of time to respond to the Official Action of November 7, 2003 for one month to March 7, 2004.

Please charge the extension fee of \$110 to Deposit Account No. 25-0120. If this fee is insufficient, the Patent Office is hereby authorized to charge any additional extension fee to Deposit Account No. 25-0120. A duplicate copy of this sheet is enclosed.

A responsive paper is filed herewith.

Respectfully submitted,

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